

## **Features**

Supports a 2x1 arrangement of Hamamatsu S13361-3050AE-08 8x8 3mm arrays for a total of 16x8 MPPCs

Signal connectors located on the back SiPM arrays located on the front

3-side tileable installation

Four encoded position signals for event centroid calculations: X+, X-, Y+, Y-

DC-coupled signal path

Low power consumption

Sum output with adjustable gain

Patented diode-coupled charge division readout, superior to traditional resistive readout

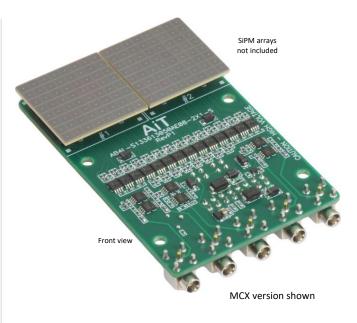
Improved spatial uniformity

Faster rise time

Reduced image noise

Precision temperature sensor

Four mounting holes for #4 or M3 hardware

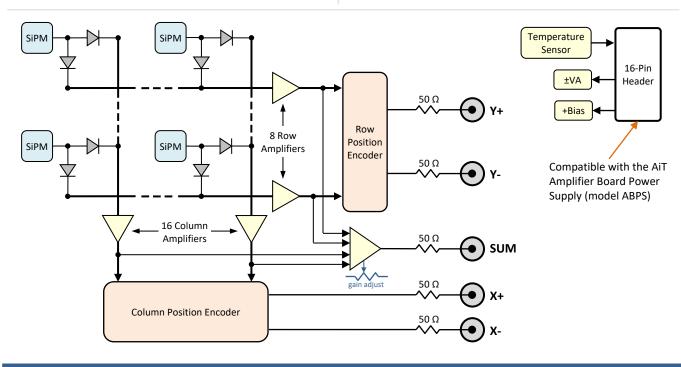


#### Part Number

AB4L-S133613008-2X1-S-P/N-XXXX

-P/N: P = Positive output signal polarity
 N = Negative output signal polarity
 XXXX: MCX, SMA, SMB, LEMO

#### Example: AB4L- S133613008-2X1-S-P-MCX Positive output signal polarity, MCX connectors



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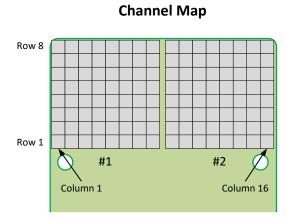


## **AB4L-S133613008-2X1-S** 4-Channel Active Base for a 2x1 Arrangement of S13361-3050AE-08 MPPC Arrays

#### Datasheet (preliminary) Rev. P1-1811

# **Specifications**

Position Signal Outputs	
Encoding	Charge division multiplexed to 4 output channels: X+, X-, Y+, Y-
Gain	750 $\Omega$ transimpedance gain
Output voltage	$0 \rightarrow -1V$ into 50 $\Omega$ load
Output impedance	50Ω
Output current	50mA maximum
Sum Signal Output	
Output voltage	$0 \rightarrow +1V$ into $50\Omega$ load (for positive output polarity)
Output impedance	50Ω
Output current	50mA maximum
Gain adjustment	x0 → x1 (row + column sum) 25-turn potentiometer
Temperature Sensor	
Output voltage	500mV + 10mV per °C
Output current	10mA
Output impedance	100Ω
Accuracy	±0.5°C
Bias Voltage	+56V typical (refer to SiPM data)
Voltage clamp	68V Zener diode 375mW maximum <u>NOTE</u> : Zener clamp is connected to the POWER connector, not the EXT BIAS connector
Amplifier Voltage (±VA)	$\pm 2.8V \rightarrow \pm 5.1V$ maximum
Current	±70mA typical (Iq, no signal, no load)
Voltage clamp	±5.1V Zener diode
Signal Connectors	50Ω coaxial options: MCX, SMA, SMB, LEMO
Power Connector	Vertical 16-pin 2-row shrouded header, 0.1" pin pitch



## **POWER Connector**

15	13	11	9	7	5	3	1
16	<b>1</b> 4	<b>1</b> 2	<b>1</b> 0	8	6	4	2

Pin	Function	Pin	Function
1	+VA	2	+VA
3	Ground	4	Ground
5	-VA	6	-VA
7	Ground	8	Ground
9	+VA Monitor	10	Ground
11	-VA Monitor	12	Ground
13	Temperature	14	Ground
15	Bias	16	Ground

#### **EXT BIAS Connector**



Pin	Function	Pin	Function
1	Ground	2	Array #1 bias
3	Ground	4	Array #2 bias

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# **4-Channel Position Encoder**

## **Column Encoder Weights**

Column# for X-	<b>Column#</b> for X+	Fraction ideal	Fraction actual	% Error	Notes
1	16	0.0625	0.0625	0.00 %	
2	15	0.1250	0.1250	0.00 %	
3	14	0.1875	0.1861	-0.75 %	
4	13	0.2500	0.2483	-0.68 %	
5	12	0.3125	0.3158	1.06 %	
6	11	0.3750	0.3731	-0.51 %	
7	10	0.4375	0.4412	0.85 %	Sum of V and V. frontions
8	9	0.5000	0.5000	0.00 %	Sum of X- and X+ fractions
9	8	0.5625	0.5618	-0.12 %	= <b>1.0625</b> Independent of signal position
10	7	0.6250	0.6250	0.00 %	
11	6	0.6875	0.6818	-0.83 %	
12	5	0.7500	0.7500	0.00 %	
13	4	0.8125	0.8021	-1.28 %	
14	3	0.8750	0.8876	1.44 %	
15	2	0.9375	0.9375	0.00 %	
16	1	1.0000	1.0000	0.00 %	

#### **Row Encoder Weights**

Row# for Y-	<b>Row#</b> for Y+	Fraction ideal	Fraction actual	% Error	Notes
1	8	0.1250	0.1250	0.00 %	
2	7	0.2500	0.2483	-0.68 %	
3	6	0.3750	0.3731	-0.51 %	Sum of Y- and Y+ fractions
4	5	0.5000	0.5000	0.00 %	= <b>1.1250</b>
5	4	0.6250	0.6250	0.00 %	Independent of signal position
6	3	0.7500	0.7500	0.00 %	independent of signal position
7	2	0.8750	0.8876	1.44 %	
8	1	1.0000	1.0000	0.00 %	

Note: Errors exclude component tolerances

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### **Output Signals**

X- = (SiPM signal) \* (encoder gain) \* (X- fraction)
X+ = (SiPM signal) \* (encoder gain) \* (X+ fraction)
Y- = (SiPM signal) \* (encoder gain) \* (Y- fraction)
Y+ = (SiPM signal) \* (encoder gain) \* (Y+ fraction)

Typical event position calculation:

 X column
 = (X + - X -) / (X + + X -) 

 Y row
 = (Y + - Y -) / (Y + + Y -)

### **Example**

SiPM signal at column 4, row 3 (excluding encoder gain)

X- = (Column 4 signal) \* 0.2483 X+ = (Column 4 signal) \* 0.8021

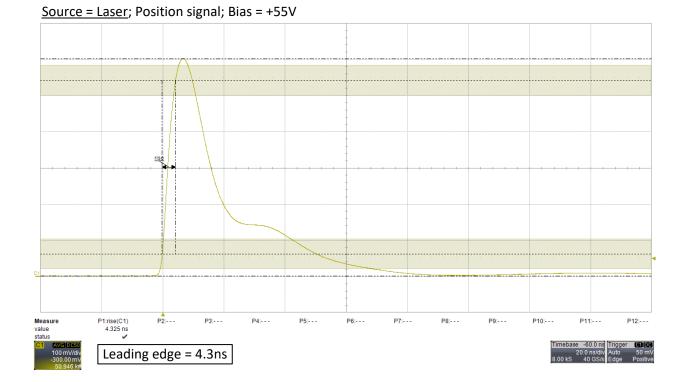
Y- = (Row 3 signal) \* 0.3731 Y+ = (Row 3 signal) \* 0.7500



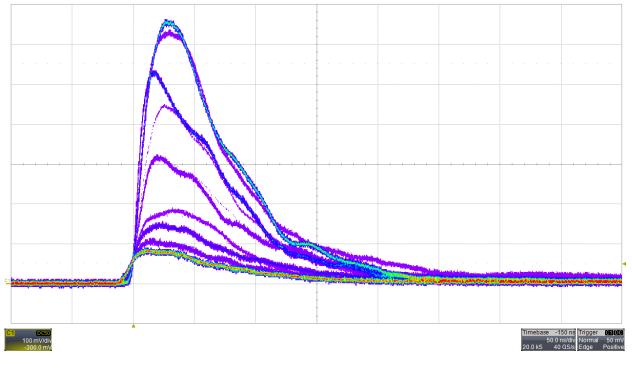
4-Channel Active Base for a 2x1 Arrangement of S13361-3050AE-08 MPPC Arrays

# **Typical Signals**

## Positive output polarity option



#### Source = LYSO; Position signal; Bias = +55V; Persistence display



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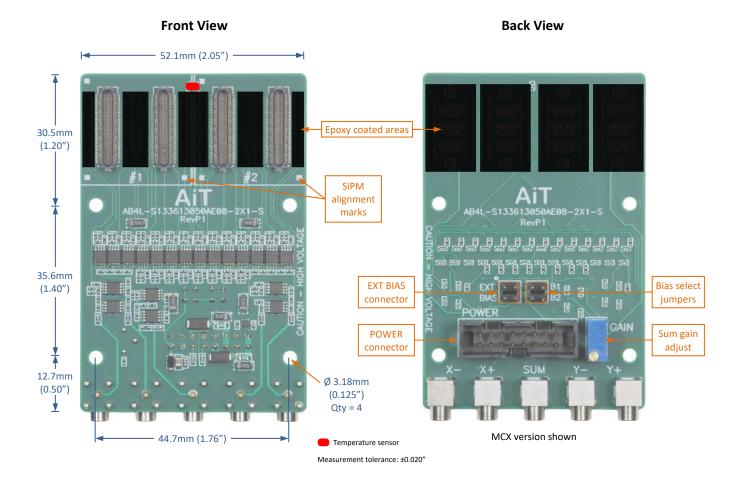
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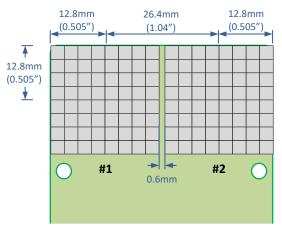
## AB4L-S133613008-2X1-S

4-Channel Active Base for a 2x1 Arrangement of S13361-3050AE-08 MPPC Arrays

## Mechanical



## **Array Locations**



#### **Bias Select Jumpers**

$\bigcirc \bigcirc$	B1
00	B2

Install jumper B1 to connect POWER connector bias voltage to Array #1.

Install jumper B2 to connect POWER connector bias voltage to Array #2.

Install B1 and B2 to apply POWER connector bias to both arrays.

Remove B1, B2, or both jumpers to apply separate bias voltages on the EXT BIAS connector.

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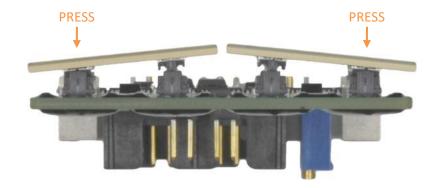


## **Array Installation Guide**

## <u>STEP 1</u>

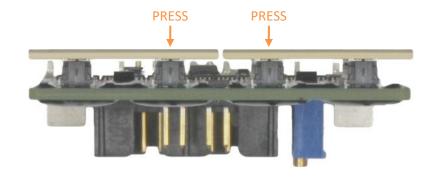
Install arrays by inserting the outer connectors first, one array at a time.

Press the arrays to insert the inner



## <u>STEP 2</u>

connectors.

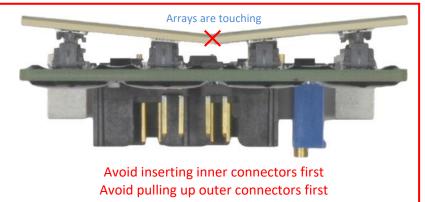


## Array Removal

To remove the arrays, reverse the installation procedure. Pull up the connectors labeled PRESS, one array at a time. Always pull up the <u>inner</u> connectors first.

#### **CAUTION**

Avoid inserting the inner connectors first or removing the outer connectors first because this may cause the array surfaces to touch. The array resin surfaces may be lightly touched together without damaging the silicon. However, excessive force can damage the resin and risk damaging the silicon.



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# **Safety Information**



- High voltage may be present during operation
- High voltage stored on capacitors may be present after power is removed
- Improper handling may result in personnel injury or equipment damage

This high-voltage device must be used only by personnel trained and qualified in safe handling, installation, and operation of high-voltage equipment.

# CAUTION – Electrostatic Discharge (ESD) Sensitivity

The circuit board can be damaged by electrostatic discharge. Observe precautions for handling electrostatic sensitive devices. Handle only at static-safe workstations.

## **High-Gain Photodetectors**

High-gain photodetectors such as silicon photomultipliers may conduct damaging currents if exposed to high optical signal levels while the bias voltage is applied, or if the bias voltage exceeds the recommended operating range. These devices must be operated only in low-light conditions, and only within the manufacturer's recommended bias voltage range.

## Handling and Disassembly

This product may be provided with a protective enclosure. Disassembled enclosure components and circuit boards may contain sharp edges. Take appropriate safety precautions while assembling or disassembling the enclosure and handling disassembled components.

## **Indoor Use Only**

Do not operate this product in a wet or damp environment. Do not operate in an explosive atmosphere.

Use of this product, and AiT Instruments' liability related to use of this product, is further governed by AiT Instruments' standard terms and conditions of sale, which were provided upon purchase of this product.