#### **Features**

Supports a 1x2 arrangement of the Onsemi ArrayC-30035-144P-PCB 12x12 arrays of 3mm SiPMs for a total of 12x24 SiPMs

Vertical signal connectors located on the back, arrays located on the front

4-side tileable installation

Four encoded position signals for event centroid calculations: X+, X-, Y+, Y-

DC-coupled signal path

Low power consumption

Patented diode-coupled charge division readout, superior to traditional resistive readout

Improved spatial uniformity

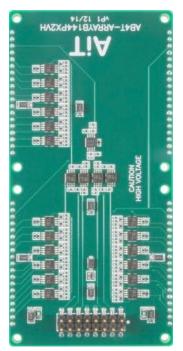
Faster rise time

Reduced image noise

Precision temperature sensor

Eight mounting holes for #2 hardware

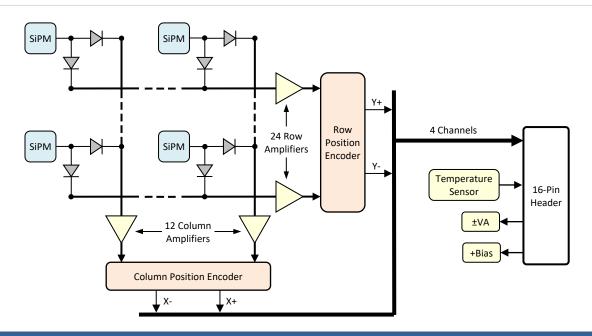




Front view

SiPM arrays not included

Back view



# **4-Channel Readout Specifications**

#### **Position Signal Outputs**

Encoding Charge division multiplexed to

4 output channels: X+, X-, Y+, Y-

Gain 750Ω transimpedance gain

Rise time < 20ns

Output voltage  $0 \rightarrow -1V$  into  $100\Omega$  load

Output impedance  $100\Omega$ 

Output current 50mA maximum

**Temperature Sensor** 

Output voltage 500mV + 10mV per °C

Average of 2 sensors

Output current 10mAOutput impedance  $100\Omega$ Accuracy  $\pm 0.5^{\circ}C$ 

Bias Voltage +29V typical (refer to SiPM data)

Voltage clamp 47V Zener diode

500mW maximum

Amplifier Voltage ( $\pm$ VA)  $\pm$ 2.8V  $\rightarrow$   $\pm$ 5.5V DC maximum

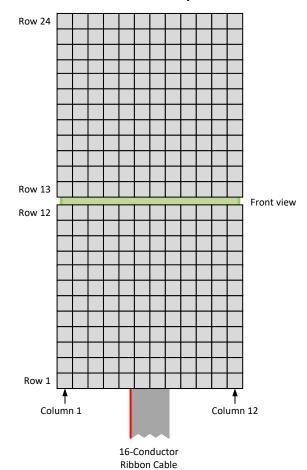
Current ±70mA typical

(Iq, no signal, no load)

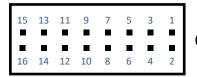
Signal Connector Vertical 16-pin 2-row header

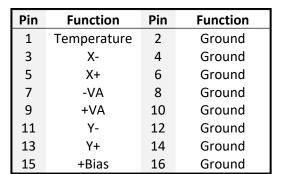
0.100" pin pitch

#### **Channel Map**



## **Signal Connector**





# **4-Channel Position Encoder**

## **Row Encoder Weights**

Row#	Row#	Fraction	Fraction	% Error	Notes
(for X- or Y-)	(for X+ or Y+)	(ideal)	(actual)		
1	24	0.0417	0.0417	0.00 %	
2	23	0.0833	0.0833	0.00 %	
3	22	0.1250	0.1250	0.00 %	
4	21	0.1667	0.1650	-1.02 %	
5	20	0.2083	0.2098	0.72 %	
6	19	0.2500	0.2483	-0.68 %	
7	18	0.2917	0.2935	0.62 %	
8	17	0.3333	0.3311	-0.66 %	Sum of X- and X+ fractions or Y- and Y+ fractions = <b>1.0417</b> Independent of signal position
9	16	0.3750	0.3731	-0.51 %	
10	15	0.4167	0.4167	0.00 %	
11	14	0.4583	0.4545	-0.83 %	
12	13	0.5000	0.5000	0.00 %	
13	12	0.5417	0.5474	1.05 %	
14	11	0.5833	0.5882	0.84 %	
15	10	0.6250	0.6250	0.00 %	
16	9	0.6667	0.6637	-0.45 %	
17	8	0.7083	0.7143	0.85 %	
18	7	0.7500	0.7500	0.00 %	
19	6	0.7917	0.7853	-0.81 %	
20	5	0.8333	0.8333	0.00 %	
21	4	0.8750	0.8876	1.44 %	
22	3	0.9167	0.9259	1.00 %	
23	2	0.9583	0.9494	-0.93 %	
24	1	1.0000	1.0000	0.00 %	

**Note:** Errors exclude component tolerances

## **Column Encoder Weights**

Row# or Col#	Row# or Col#	Fraction	Fraction	% Error	Notes
(for X- or Y-)	(for X+ or Y+)	(ideal)	(actual)	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	
1	12	0.0833	0.0833	0.00 %	
2	11	0.1667	0.1650	-1.02 %	
3	10	0.2500	0.2483	-0.68 %	
4	9	0.3333	0.3311	-0.66 %	
5	8	0.4167	0.4167	0.00 %	Sum of X- and X+ fractions
6	7	0.5000	0.5000	0.00 %	or Y- and Y+ fractions
7	6	0.5833	0.5882	0.84 %	= 1.0833
8	5	0.6667	0.6637	-0.45 %	Independent of signal position
9	4	0.7500	0.7500	0.00 %	
10	3	0.8333	0.8333	0.00 %	
11	2	0.9167	0.9091	-0.83 %	
12	1	1.0000	1.0000	0.00 %	

**Note:** Errors exclude component tolerances

#### **Output Signals**

X- = (SiPM signal) \* (encoder gain) \* (X- fraction)

**X+** = (SiPM signal) \* (encoder gain) \* (X+ fraction)

Y- = (SiPM signal) \* (encoder gain) \* (Y- fraction)

Y+ = (SiPM signal) \* (encoder gain) \* (Y+ fraction)

Typical event position calculation:

X column = (X + - X -) / (X + + X -)

Y row = (Y+ - Y-) / (Y+ + Y-)

#### **Example**

SiPM signal at column 4, row 3 (excluding encoder gain)

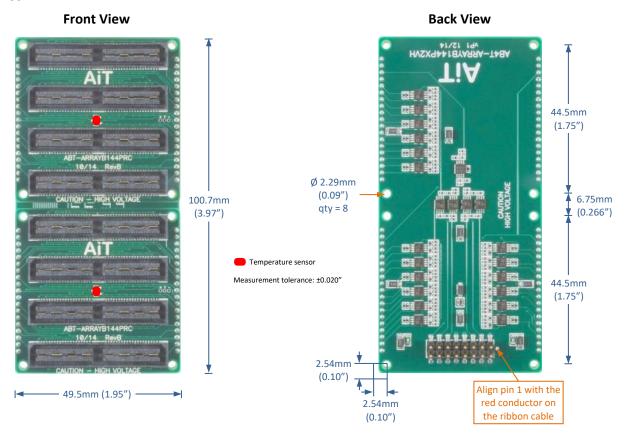
X- = (Column 4 signal) \* 0.3311

X+ = (Column 4 signal) \* 0.7500

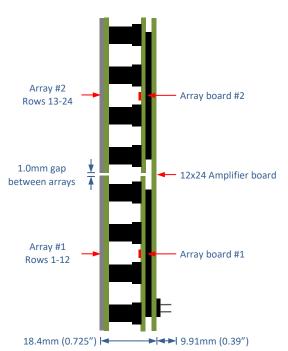
Y- = (Row 3 signal) \* 0.1250

**Y+** = (Row 3 signal) \* 0.9259

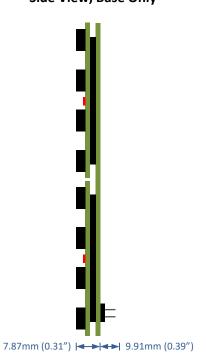
## Mechanical



#### Side View, Base Attached to Array

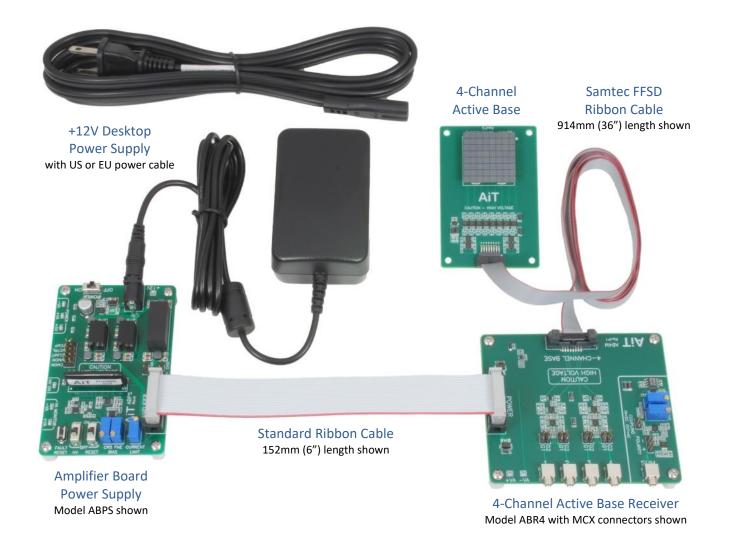


#### Side View, Base Only



4-Channel Active Base for a 1x2 Arrangement of the Onsemi ArrayC-30035-144P-PCB

### **4-Channel Active Base Readout Kit**



#### Components

Each component is available separately. Refer to each datasheet for details.

The Active Base includes a 914mm (36") Samtec FFSD micro-pitch ribbon cable.

The Amplifier Board Power Supply includes a 12V desktop power supply and a HV80 bias voltage power supply.

The 4-channel Active Base Receiver includes a 152mm (6") power supply ribbon cable and a breakout board to connect any external power supply.

4-Channel Active Base for a 1x2 Arrangement of the Onsemi ArrayC-30035-144P-PCB

# **Safety Information**



# WARNING – High Voltage

- High voltage may be present during operation
- High voltage stored on capacitors may be present after power is removed
- Improper handling may result in personnel injury or equipment damage

This high-voltage device must be used only by personnel trained and qualified in safe handling, installation, and operation of high-voltage equipment.



# **CAUTION** – Electrostatic Discharge (ESD) Sensitivity

The circuit board can be damaged by electrostatic discharge. Observe precautions for handling electrostatic sensitive devices. Handle only at static-safe workstations.

# **High-Gain Photodetectors**

High-gain photodetectors such as silicon photomultipliers may conduct damaging currents if exposed to high optical signal levels while the bias voltage is applied, or if the bias voltage exceeds the recommended operating range. These devices must be operated only in low-light conditions, and only within the manufacturer's recommended bias voltage range.

# **Handling and Disassembly**

This product may be provided with a protective enclosure. Disassembled enclosure components and circuit boards may contain sharp edges. Take appropriate safety precautions while assembling or disassembling the enclosure and handling disassembled components.

## **Indoor Use Only**

Do not operate this product in a wet or damp environment. Do not operate in an explosive atmosphere.

Use of this product, and AiT Instruments' liability related to use of this product, is further governed by AiT Instruments' standard terms and conditions of sale, which were provided upon purchase of this product.